

X-1192 US  
10/600,848

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PATENT  
Conf. No.: 7556

IN THE UNITED STATES PATENT OFFICE

Applicants: Jonathan B. Ballagh et al.  
Assignee: Xilinx, Inc.  
Title: Clock Stabilization Detection For Hardware Simulation  
Serial No.: 10/600,848 File Date: 06/19/2003  
Examiner: Andre Pierre Louis Art Unit: 2123  
Docket No.: X-1192 US Conf. No.: 7556

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Mail Stop AMENDMENT  
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AMENDMENT IN RESPONSE TO FINAL OFFICE ACTION AFTER RCE

In response to the Final Office Action after the filing of an RCE mailed from the Patent Office on March 18, 2008, please replace/substitute the following claim(s) as indicated.

**Amendments to the Claims** are reflected in the listing of claims which begins on page 2 of this paper.

**Remarks** begin on page 9 of this paper.

Enter with RCE 6-18-08

WAL  
6/26/08